

## ARRAY PREFETCH APPARATUS AND METHOD

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### ABSTRACT OF THE DISCLOSURE

5 An array prefetch system improves processor performance by automatically tuning a statically compiled and compacted loop program at run-time to accommodate variations in latency of memory read operations. Using the array prefetch system, the processor, while awaiting completion of a data access, continues to generate requests for subsequent iterations rather than fully halting execution until a read access is  
10 finished.